

Deenbandhu Chhotu Ram University of Science & Technology, Murthal (Sonapat)
Department of Electronics & Communication Engineering
SCHEME OF STUDIES & EXAMINATIONS
B.Tech. IVth YEAR (SEMESTER –VII)
Choice Based Credit Scheme w.e.f. 2021-22

S. No.	Course No.	Course Title	Teaching Schedule			Marks of Class work	Examination Marks		Total	Credit	Duration of Exam
			L	T	P		Theory	Practical			
1		Program Elective-3	3	0	0	25	75	-	100	3	3
2		Program Elective-4	3	0	0	25	75	-	100	3	3
3		Program Elective-5	3	0	0	25	75	-	100	3	3
4	ECE401C	Fiber Optic Systems & Networks	3	0	0	25	75	-	100	3	3
5	ECE481C	Fiber Optic Systems & Networks Lab	0	0	2	25	-	75	100	1	3
6	ECE483C	Project Stage-I	0	0	8	25	-	75	100	4	3
7	ECE485C	Professional Training (Level-3)	0	0	2	100	-	-	100	2	-
8		Open Elective-II	3	0	0	25	75	-	100	3	3
Total			15	0	12	275	375	150	800	22	

Note:

- Assessment of Professional Training (Level-3)(ECE487C), undergone at the end of semester-VI, will be based on seminar, viva-voce, report and certificate of professional training obtained by the student from the industry / institute / research lab / training centre etc.
- (i) Project coordinator and other assisting co-coordinators will be assigned the Project Stage-I (ECE485C) load of, maximum of 02 hrs. per week including their own guiding load of one hr. However, the guiding teacher will be assigned maximum of one period of teaching load irrespective of number of students/groups under him/her.
(ii)*Project involving design, fabrication, testing, computer simulation, case studies etc., will be commenced by students in semester-VII and will be completed in semester-VIII (***applicable for those students only who will not opt for internship in VIII Semester**).
- Students will be permitted to opt for any three elective courses from the list given below. The minimum strength of the students should be 20 to run an elective course.

S.No.	Program Elective-3		Program Elective-4		Program Elective-5	
	Course No.	Course Title	Course No.	Course Title	Course No.	Course Title
1	ECE421C	Adaptive Signal Processing	ECE429C	Wavelets	ECE437C	Fundamentals of Image Processing
2	ECE423C	Mobile Communication & Networks	ECE431C	Wireless Sensor Networks	ECE439C	Satellite Communication
3	ECE425C	Cryptography	ECE433C	Pattern Recognition	ECE441C	Artificial Neural Networks & Fuzzy Logic
4	ECE427C	High Speed Electronics	ECE435C	Biomedical Signal Processing	ECE443C	ASICs and FPGAs

- Students will be permitted to opt for any one **Open Elective-II** course run by other department, from group of subjects given in table below. However, the department shall offer those elective for which they have expertise. The choice of the students for any elective shall not be binding for the department to offer, if the department does not have expertise. The minimum strength of the students should be 20 to run an elective course.

S. No.	Course No.	Course Title	Teaching Schedule			Marks of Class work	Examination Marks		Total	Credit	Duration of Exam
			L	T	P		Theory	Practical			
1	CSE305C	Computer Networks	3	0	0	25	75	-	100	3	3
2	CSE431C	Cyber Security	3	0	0	25	75	-	100	3	3
3	CHE457C	Industrial Safety	3	0	0	25	75	-	100	3	3
4	CE406C	Disaster Management	3	0	0	25	75	-	100	3	3
5	ECE327C	Consumer Electronics	3	0	0	25	75	-	100	3	3

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

Deenbandhu Chhotu Ram University of Science & Technology, Murthal (Sonapat)
Department of Electronics & Communication Engineering
SCHEME OF STUDIES & EXAMINATIONS
B.Tech. IVth YEAR (SEMESTER –VIII)
Choice Based Credit Scheme w.e.f. 2021-22

S. No.	Course No.	Course Title	Teaching Schedule			Marks of Class work	Examination Marks		Total	Credit	Duration of Exam
			L	T	P		Theory	Practical			
1		Program Elective-6	3	0	0	25	75	-	100	3	3
2		Program Elective-7	3	0	0	25	75	-	100	3	3
3	ECE402C	Information Theory & Coding	3	0	0	25	75	-	100	3	3
4		Open Elective-III	3	0	0	25	75	-	100	3	3
5	ECE484C	Project Stage-II	0	0	16	25	-	75	100	8	-
6	GFECE490C	General Fitness for the Profession	-	-	-	-	-	100	100	-	-
Total			12	0	16	125	300	175	600	20	

Or

S. No.	Course No.	Course Title	Teaching Schedule			Marks of Class work	Examination Marks		Total	Credit	Duration of Exam
			L	T	P		Theory	Practical			
1	ECE488C	Internship	-	-	-	250	-	250	500	20	-
2	GFECE490C	General Fitness for the Profession	-	-	-	-	-	100	100	-	-
Total			-	-	-	250	-	350	600	20	

Note:

- General Fitness For The Profession (GFECE490C) is a compulsory & qualifying course (**Audit Pass**) under which student will be evaluated for his performance in all types of activities like Academics, Cultural, Sports, NSS, organisation of camps, social activities etc., during his all 8 semesters, at the end of 8th semester. Regarding this course student will be motivated during the induction programme at the time of admission, so that he/she will be vigilant for motivation towards these activities. The evaluation of the student for his / her General Fitness for the Profession will be carried out the committee of examiners constituted as under :-
 - Chairperson of the Department : Chairperson
 - Final Year Coordinator of the Department : Member
 - External Examiner : Appointed by the University
- (i) Project coordinator and other assisting co-coordinators will be assigned the Project Stage-II (**ECE484C**) load of, maximum of 02 hrs. per week including their own guiding load of one hr. However, the guiding teacher will be assigned maximum of one period of teaching load irrespective of number of students/groups under him/her.
 (ii) * Project involving design, fabrication, testing, computer simulation, case studies etc., which has been commenced by students in semester-VII will be completed in semester –VIII (***applicable for those students only who will not opt for internship in VIII Semester**).
- Students can opt for (minimum 16 weeks) Internship (**ECE488C**) in lieu of 8th Semester subjects for equivalent credits (20 credits), after fulfilling the conditions specified in “**Internship Guidelines**”.
- Students will be permitted to opt for any two elective courses from the list given below. The minimum strength of the students should be 20 to run an elective course.

S.No	Program Elective-6		Program Elective-7	
	Course No.	Course Title	Course No.	Course Title
1	ECE422C	Radar & Sonar Engineering	ECE430C	Reliability Engineering
2	ECE424C	Information Security	ECE432C	PLC & SCADA Systems
3	ECE426C	Telecommunication Switching Systems	ECE434C	CAD for VLSI
4	ECE428C	Digital Image Processing for Medical Applications	ECE436C	Multimedia Communication

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

5. Students will be permitted to opt for any one **Open Elective-III** course run by other department, from group of subjects given in table below. However, the department shall offer those elective for which they have expertise. The choice of the students for any elective shall not be binding for the department to offer, if the department does not have expertise. The minimum strength of the students should be 20 to run an elective course.

S. No.	Course No.	Course Title	Teaching Schedule			Marks of Class work	Examination Marks		Total	Credit	Duration of Exam
			L	T	P		Theory	Practical			
1	CSE340C	Artificial Intelligence & Expert Systems	3	0	0	25	75	-	100	3	3
2	EE452C	Electrical And Hybrid Vehicles	3	0	0	25	75	-	100	3	3
3	MGT401C	Entrepreneurship	3	0	0	25	75	-	100	3	3
4	ME452C	Fundamentals of Sustainable Manufacturing	3	0	0	25	75	-	100	3	3
5	CHE459C	Nano-Science And Nano-Technology	3	0	0	25	75	-	100	3	3
6	EE454C	Smart Grid	3	0	0	25	75	-	100	3	3

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE401C Fiber Optic Systems & Networks

B.Tech. 4th YEAR (SEMESTER –VII)

Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (9 Lectures)

Introduction to Optical Communication System: Electromagnetic Spectrum used for optical communication, block diagram of optical communication system. Introduction to vector nature of light, propagation of light, propagation of light in a cylindrical dielectric rod, Ray model, wave model, Advantages of optical fiber Communication.

Unit 2 (12 Lectures)

Structure of Optical Fibers: Different types of optical fibers (Step Index Fibers, Graded index Fibers, Single mode fibers, Multi Mode fibers) Modal analysis of a step index fiber. Signal degradation on optical fiber due to dispersion and attenuation. Fabrication of fibers and measurement techniques like OTDR.

Unit 3 (12 Lectures)

Optical sources :LEDs and Lasers, Photo-detectors - pin-diodes, APDs, detector responsivity, noise, optical receivers. Optical link design - BER calculation, quantum limit, power penalties. Optical switches - coupled mode analysis of directional couplers, electro-optic switches.

Optical amplifiers :EDFA, Raman amplifier.

Unit 4 (9 Lectures)

WDM and DWDM systems: Principles of WDM networks. Nonlinear effects in fiber optic links. Concept of self-phase modulation, group velocity dispersion and soliton based communication.

Text/Reference Books:

1. John. M Senior, Optical Fiber Communication, 3rd Edition, Pearson Education.
2. G. Keiser, Fiber Optic communication, McGraw-Hill
3. T. Tamir, Integrated optics, Springer-Verlag.
4. G. Agrawal, Nonlinear fiber optics, Academic Press.
5. G. Agrawal, Fiber optic Communication Systems, John Wiley and sons.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understanding principles of fiber optic communication, comparing it to contemporary means of communication. Also understand spectrum utilization sensitizing them towards natural resources.
2. Comparing and selecting the right fiber for a specific application by measuring losses in the fiber. They learn to use instruments for quick restoration of a fiber network in case of failure.
3. Significance of available sources of light and detectors for efficient usage and protection of environment is understood.
4. Enhancing data rate by multiplexing techniques taking our system towards next level generation.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:

Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE481C Fiber Optic Systems & Networks Lab

B.Tech. 4th YEAR (SEMESTER –VII)

Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
0	0	3	1.5	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

List of Experiments:

- 1 To calculate the attenuation losses in optical fiber.
- 2 To calculate the bending losses in optical fiber.
- 3 To study various type of dispersion and calculating dispersion in optical fiber.
- 4 To enhance the system capacity using WDM and analyze results using BER and Eye Pattern.
- 5 To study the characteristics of fiber Brag Grating.
- 6 To simulate dispersion analysis in different types of fibers.
- 7 To simulate the non-linear effect in optical fiber.
- 8 To study and simulate the characteristics of LEDs.
- 9 To study and simulate the characteristics of LASER diode.
- 10 To study and simulate characteristics of PIN photodiode.

Text/Reference Books:

- 1 John. M Senior, Optical fiber Communication: Principal and Practice, 3rd Edition, Pearson Education.
- 2 G. Keiser, Fiber Optic communication, McGraw-Hill
- 3 T. Tamir, Integrated optics, Springer-Verlag
- 4 G. Agrawal, Nonlinear fiber optics, Academic Press
- 5 G. Agrawal, Fiber optic Communication Systems, John Wiley and sons

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. To be able to work on hardware kits of fiber optics, understanding fragile structure of fiber hence learning proper measures for deployment.
2. Students learn different instruments used for establishing an optical link without degrading environment.
3. A simulation environment is setup for thorough analysis and evaluation of a link so that new techniques in the field could be developed.
4. Different sources and detectors could be simulated with different parameters so that their characterization could be done. This may bring about change in technology.

Note:

1. Each laboratory class/section shall not be more than about 20 students.
2. To allow fair opportunity of practical hands on experience to each student, each experiment may be either done by each student individually or in group of not more than 3-4 students. Larger groups be strictly discouraged/disallowed.
3. Ten experiments are to be performed out of which at least seven experiments should be performed from the above list. Remaining three experiments should be performed from the above list or designed and set by the concerned department as per the scope of the syllabus.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE421C Adaptive Signal Processing

B.Tech. 4th YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (11 Lectures)

Adaptive Systems: Definition, Characteristics, Areas of Application, General Properties, Open and Closed- Loop Adaptation, Applications of Closed-Loop Adaptation, Example of an Adaptive System.

The Adaptive Linear Combiner: General Description, Input Signal and Weight Vectors, Desired Response and Error, The Performance Function, Gradient and minimum mean-square Error, Example of a performance surface, Alternative Expression of the Gradient, Decorrelation of error and input Components.

Unit 2 (10 Lectures)

Theory of Adaptation with Stationary Signals: Review of probability, random variables and stationary random processes, Correlation structures, Normal Form of input Correlation matrix, Eigen values and Eigenvectors of the input Correlation matrix, an example with two weights, Geometrical significance of Eigen values and Eigen vectors.

Unit 3 (12 Lectures)

Searching the performance surface: Methods of searching the performance surface, basic ideas of Gradient search methods, simple Gradient search algorithm and its solution, Stability and Rate of Convergence, The learning curve, Gradient search by Newton's method, Newton's method in multidimensional space, Gradient search by the Steepest Descent, Comparison of learning curves.

Unit 4 (12 Lectures)

Adaptive Algorithms: LMS Algorithm, derivation of the LMS algorithm, Convergence of the weight vector, example of convergence; Z-Transform in adaptive signal processing: Z-Transform, Right and Left handed sequences, Transfer functions, Frequency response, impulse response and stability, inverse Z-transform, Correlation Functions and Power Spectra.

Text/Reference Books:

1. S. Haykin, Adaptive filter theory, 4th Edition, Pearson Education
2. B.Widrow and S.D. Stearns, Adaptive Signal Processing, Pearson Education.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand open & Closed loop Control systems and their practical applications.
2. Use non-linear control and concepts of need & significance of changing the control parameters w.r.t. real-time situation.
3. Mathematically represent the 'adaptability requirement' and use in practical applications,
4. Understand the mathematical treatment for Adaptive Algorithms and Adaptive signal processing.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines "AICTE Examination Reforms". Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE423C Mobile Communication & Networks

B.Tech. 4th YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1(12 Lectures)

Mobile Radio System: A reference model, frequencies for radio transmission, Signals, antennas, signal propagation, multiplexing.

Characteristics Of Radio Waves: Multipath characteristics of radio waves signal fading, time dispersion, Doppler spread, coherence time, LCR, fading statistics, diversity techniques

Unit 2(12 Lectures)

Mobile Radio Propagation: Mechanism, free space path loss, long distance path loss model, Okumara model, Hata model, PCS model, wideband PCS, microcell model, indoor propagation model, Jake's channel model.

Wireless Systems: Standards – GSM, signaling & call control, mobility management, location racking wireless data services IS-95, GPRS and CDMA.

Unit 3(10 Lectures)

Modulation Technology:System application, FM for mobile radio, digital modulation, constant envelop modulation, nonconstant envelop modulation, OFDM modem, spread spectrums systems.

Wireless Data Networking: IEEE standards, models different layers, wireless LAN, Hypes LAN, Bluetooth performance analysis of link & transport layer protocols over wireless channels.

Unit 4(10 Lectures)

Mobile Network Layer: Mobile IP: Goals, assumptions & requirements, IP packet delivery, agent discovery, registration, tunneling and encapsulation, optimization, reverse tunneling, IP-V6, mobile ad-hoc networks.

Mobile Transport Layer: Tradition TCP, classical TCP improvement, TCP over 2.5G/3G wireless networks performance enhancing proxies.

Text/ Reference Books:

1. Mobile Communication: Jochen Schiller Pearson Education.
2. Rajeshwar Dass, "Wireless Communication Systems," I.K International Pvt. Ltd
3. William C.Y.Lee, "Mobile Cellular Telecommunications Analog and Digital Systems", 2nd edition, TMH, 1995.
4. Asha Mehrotra, "A GSM system Engineering" Artech House Publishers Bosten, London,1997.
5. T.S.Rappaport, "Wireless Communications Principles and Practice", 2nd edition, PHI,2002.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand basics of mobile communication and propagation mechanism of wave which will help the students to understand propagation model of wireless communication systems
2. Understand various terms/parameters which should be kept in mind while designing the mobile communication model which in turn helps in capacity enhancement of wireless communication systems.
3. Differentiate between working of analog and digital modulation techniques to solve the problem of quality of service prevailing in society.
4. Understand various standards of mobile communication systems i.e. GSM, SDMA,IS-95 etc. which automatically improve the selection of mobile communication standards in day to day life. Students will also be able to understand the concept of network and transport layer used in mobile communication standards.

Note:

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE425C Cryptography
B.Tech. 4th YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits
3	0	0	3

Class Work	: 25
Examination	: 75
Total	: 100
Duration of Exam	: 3Hours

Unit 1 (10 Lectures)

Basic Concepts of Number Theory and Finite Fields: Divisibility and the divisibility algorithm, Euclidean algorithm, Modular arithmetic, Groups, Rings and Fields, Finite fields of the form GF(p), Polynomial arithmetic, Finite fields of the form GF(2ⁿ)

Unit 2 (11 Lectures)

Classical Encryption Techniques and Symmetric Ciphers : Symmetric cipher model, Substitution techniques, Transposition techniques, Steganography : Traditional Block Cipher structure, Data Encryption Standard, The AES Cipher

Unit 3 (11 Lectures)

Pseudo-Random-Sequence Generators and Stream Ciphers: Linear Congruential Generators, Linear Feedback Shift Registers, Design and analysis of stream ciphers, Stream ciphers using LFSRs

Unit 4 (12 Lectures)

Public-Key Encryption and Hash Functions: Prime Numbers, Fermat's and Euler's theorem, Primality testing, Chinese Remainder theorem, discrete logarithm. The RSA algorithm, Diffie -Hellman Key Exchange, Elliptic Curve Arithmetic, Elliptic Curve Cryptography. Background, Snefru, N-Hash, MD4, MD5, Secure Hash Algorithm [SHA], One way hash functions using symmetric block algorithms, Using public key algorithms, Choosing a one-way hash functions, Message Authentication Codes. Digital Signature Algorithm, Discrete Logarithm Signature Scheme

Text/Reference Books:

1. William Stallings , "Cryptography and Network Security Principles and Practice", Pearson Education Inc., 6th Edition, 2014, ISBN: 978-93-325-1877-3
2. Bruce Schneier, "Applied Cryptography Protocols, Algorithms, and Source code in C", Wiley Publications, 2nd Edition, ISBN: 9971-51-348-X
3. Cryptography and Network Security, Behrouz A. Forouzan, TMH, 2007.
4. Cryptography and Network Security, Atul Kahate, TMH, 2003.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Use basic cryptographic algorithms to encrypt the data.
2. Generate some pseudorandom numbers required for cryptographic applications.
3. Provide authentication and protection for encrypted data.
4. Understand various authentication algorithms.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines "AICTE Examination Reforms". Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE427C High Speed Electronics

B.Tech. 4th YEAR (SEMESTER –VII)

Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (11 Lectures)

Transmission line theory (basics) crosstalk and non-ideal effects; signal integrity: impact of packages, vias, traces, connectors; non-ideal return current paths, high frequency power delivery, methodologies for design of high speed buses; radiated emissions and minimizing system noise.

Unit 2 (10 Lectures)

Noise Analysis: Sources, Noise Figure, Gain compression, Harmonic distortion, Inter-modulation, Cross-modulation, Dynamic range Devices: Passive and active, Lumped passive devices (models), Active (models, low vs high frequency)

Unit 3(12 Lectures)

RF Amplifier Design, Stability, Low Noise Amplifiers, Broadband Amplifiers (and Distributed) Power Amplifiers, Class A, B, AB and C, D E Integrated circuit realizations, Cross-over distortion Efficiency RF power output stages Mixers –Up-conversion, Down-conversion, Conversion gain and spurious response. Oscillators Principles.

Unit 4 (11 Lectures)

PLL Transceiver architectures. Printed Circuit Board Anatomy, CAD tools for PCB design, Standard fabrication, Microvia Boards.

Board Assembly: Surface Mount Technology, Through Hole Technology, Process Control and Design challenges.

Text/Reference Books:

1. Stephen H. Hall, Garrett W. Hall, James A. McCall “High-Speed Digital System Design: A Handbook of Interconnect Theory and Design Practices”, August 2000, Wiley-IEEE Press
2. Thomas H. Lee, “The Design of CMOS Radio-Frequency Integrated Circuits”, Cambridge University Press, 2004, ISBN 0521835399.
3. Behzad Razavi, “RF Microelectronics”, Prentice-Hall 1998, ISBN 0-13-887571-5.
4. Guillermo Gonzalez, “Microwave Transistor Amplifiers”, 2nd Edition, Prentice Hall.
4. Kai Chang, “RF and Microwave Wireless systems”, Wiley.
5. R.G. Kaduskar and V.B. Baru, Electronic Product design, Wiley India, 2011 N.H.E.

Course Outcomes: At the end of the course the students will be able to:

1. Understand significance and the areas of application of high-speed electronics circuits.
2. Understand the properties of various components such as Noise figure, gain compression, inter-modulation, extra modulation etc. used in high speed electronics circuit design
3. Design High-speed electronic components : RF power amplifier, RF mixer and Oscillators.
4. Study High speed PLL system and tool used to design these high speed components & PLL system.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students: Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE429C Wavelets
B.Tech. 4th YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1(10 Lectures)

Continuous Wavelet Transform: Introduction, definition, CWT as a correlation, CWT as an operator, Inverse CWT, Introduction to discrete wavelet transform, Orthogonal wavelet decomposition.

Unit 2(12 Lectures)

MRA, Orthogonal Wavelets, Construction of general Orthonormal MRN, Wavelet basis of MRN, Digital filtering interpretation, Interpreting Orthonormal MRA's for Discrete – time signals, Issues related to PRQMF Filter banks, , Generating scaling function and Wavelets from Filter Banks.

Unit 3(12 Lectures)

Wavelet Transform and Data Compression, Introduction, Transform Coding, DTWT for image compression, Image compression using DTWT and Run length coding, Embedded tree image coding, comparison with JPEG, Audio compression, Audio masking, Wavelet based audio coding.

Unit 4(10 Lectures)

Applications of Wavelet Transforms: Introduction, Wavelet Denoising, Speckle Removal, Edge Detection and Object isolation, Image Fusion, Object detection by Wavelet Transforms of Projections, Communication Applications-Scaling Functions as signaling Pulses, Discrete Wavelet Multitone Modulation.

Text/Reference Books:

1. Raghuvver M. Rao, Ajit S. Bopardikar, “Wavelet Transforms: Introduction to Theory and Applications”, Pearson Education.
2. Y.T. Chan, Wavelet Basics, Kluwer Publishers, Boston, 1993.
3. Daubechies, Ten Lectures on Wavelets, Society for Industrial and Applied Mathematics, Philadelphia, PA, 1992.
4. C. K. Chui, An Introduction to Wavelets, Academic Press Inc., New York, 1992.
5. Gerald Kaiser, A Friendly Guide to Wavelets, Birkhauser, New York, 1995.
6. P. P. Vaidyanathan, Multirate Systems and Filter Banks, Prentice Hall, New Jersey, 1993.
7. A.N. Akansu and R.A. Haddad, Multiresolution signal Decomposition: Transforms, Subbands and Wavelets, Academic Press, Oranld, Florida, 1992.
8. B.Boashash, Time-Frequency signal analysis, In S.Haykin, (editor), Advanced Spectral Analysis, pages 418--517. Prentice Hall, New Jersey, 1991.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand time-frequency nature of the signals and using wavelet transform as an alternative and more useful transform.
2. Understand multi resolution analysis and apply the concepts of wavelets to practical problems like compression.
3. Mathematically analyze the systems or process the signals using appropriate wavelet functions.
4. Using and selecting appropriate resolution for practical problems; preprocessing like noise removal from images.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE431C Wireless Sensor Networks

B.Tech. 4th YEAR (SEMESTER –VII)

Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1(9 Lectures)

Introduction to Sensor Networks, Unique constraints and Challenges in sensor networking, Advantage of Sensor Networks, Applications of Sensor Networks, Types of wireless sensor networks.

Unit 2(12 Lectures)

Mobile Ad-hoc Networks (MANETs), Vehicular Ad-hoc Networks (VANETs) and Wireless Sensor Networks, Single-hop and multi-hop wireless communication, Enabling technologies for Wireless Sensor Networks. Issues and challenges in wireless sensor networks. Routing protocols, MAC protocols: Classification of MAC Protocols, S-MAC Protocol, B-MAC protocol, IEEE 802.15.4 standard and ZigBee,

Unit 3(9 Lectures)

Dissemination protocol for large sensor network. Data dissemination, data gathering, and data fusion; Quality of a sensor network; Real-time traffic support and security protocols.

Unit 4(10 Lectures)

Design Principles for WSNs, Gateway Concepts, Need for gateway, WSN to Internet Communication and Internet to WSN Communication. single-node architecture, hardware components & design constraints, operating systems and execution environments, introduction to Tiny OS.

Text/Reference Books:

1. WalteneusDargie , Christian Poellabauer, "Fundamentals Of Wireless Sensor Networks Theory And Practice", By John Wiley & Sons Publications ,2011.
2. SabrieSoloman, "Sensors Handbook" by McGraw Hill publication. 2009.
3. Feng Zhao, Leonidas Guibas, "Wireless Sensor Networks", Elsevier Publications,2004 .
4. Kazem Sohrby, Daniel Minoli, "Wireless Sensor Networks": Technology, Protocols and Applications, Wiley-Inter science.
5. Philip Levis, And David Gay "TinyOS Programming" by Cambridge University Press 2009.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Design wireless sensor networks for a given application.
2. Understand emerging research areas in the field of sensor networks.
3. Understand MAC protocols used for different communication standards used in WSN.
4. Explore new protocols for WSN.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:

Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines "AICTE Examination Reforms". Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE433C Pattern Recognition

B.Tech. 4th YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (9 Lectures)

Feature Extraction and Representation: Image Features-Image Histogram, Image Transformation, Edge detection, Image segmentation, Hough Transforms; Feature representation-Chain code representation, polygonal Approximation, Boundary Segments, Approximation Curve, Different Measurements, shape descriptors; Measuring moments, Principal Components, Boundary thinning, Measuring Textures: using statistical moments, structural approach, spectral approach; Biometric Features-Fingerprint Recognition; Handwritten Script.

Unit 2 (10 Lectures)

Pattern Recognition: Recognition and Learning-Human Learning, Machine Learning; Recognition by Machines; Overview of Pattern Recognition-Acquisition and Representation of Patterns, Feature Selection, Pattern Classification; Applications of Pattern Recognition; Pattern Recognition Strategies-Statistical Pattern Recognition, Syntactic Pattern Recognition; Optimization Methods used for Pattern Recognition: Overview of Simulated Annealing, Overview of Evolutionary Algorithms, Genetic Algorithms, Evolutionary programming, Annealing Evolution.

Unit 3 (10 Lectures)

Classification & Decision Making: Decision Making-Linear Functions, Non Linear Separability, Generation of Decision Functions, Generalized Decision Functions, Distance based Classifier
Statistical Decision Making: Bayes Theorem for Decision Making, Simple Bayes Classification, Probability and Expected Values, Likelihood Estimation, Decision Regions and Decision Boundaries, Multivariate Decision making, Estimation of Error Rates, Minimizing Risk Factor.

Unit 4 (12 Lectures)

Nearest Neighbour Classifier: Distance based Classification, The Nearest Neighbour classifier, Nearest Neighbour Error Rate, Performance of Nearest Neighbour Classifier, NN Learning in Discrete and Continuous Domains, Important Features of NN Algorithm, Modification for Non-Numeric Attributes

Decision Tree Learning: Tree and decision Tree with example, Decision Tree Construction and Learning, Performance of Decision Tree Learning, Determining the best Splitting Attribute, Some common Decision Tree Algorithms.

Text Books:

1. Malay K. Pakhira, Digital Image Processing and Pattern Recognition, PHI Learning, 2011
2. R.O.Duda, P.E.Hart and D.G.Stork, *Pattern Classification*, John Wiley, 2001

References:

1. K. R. Castleman, *Digital Image Processing*, Prentice Hall of India, 1996.
2. S.Theodoridis and K.Koutroumbas, *Pattern Recognition*, 4/e, Academic Press, 2009.
3. C.M.Bishop, *Pattern Recognition and Machine Learning*, Springer, 2006.
4. W.Chou B.H. Juang (Eds.), *Pattern Recognition in Speech and Language Processing*, CRC Press, 2003.
5. J.I.Tou & R.C.Gonzalez, *Pattern Recognition Principles*, Addison-Wesley.
6. R.Schalkoff, *Pattern Recognition –Statistical, Structural and Neural Approach*.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand and recognize image features, different transforms, biometric features and their use for various applications.
2. Recognize patterns and understand machine learning. They will know about evolutionary algorithms and their programming.
3. Classify and decision making using different algorithms and functions including statistical methods.
4. Understand and apply different types of classifiers such as nearest neighbor, distance based, and Neural Networks etc. they will also know about decision tree learning, algorithms and performances.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE435C Biomedical Signal Processing

B.Tech. 4th YEAR (SEMESTER –VII)

Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (9 Lectures)

Introduction to Biomedical Signals: Nature of Biomedical signals, Examples of Biomedical signals-Action Potential, The Electroneurogram (ENG), The Electromyogram (EMG), The electrocardiogram (ECG), The Electroencephalogram (EEG), Event-related potentials (ERP), The electrogastrogram (EGG), the Phonocardiogram (PCG), Carotid pulse, Signals from catheter tip sensors, speech signal, Objectives of Biomedical signal analysis, Difficulties in Biomedical signal analysis, Computer –aided diagnosis.

Unit 2 (10 Lectures)

Filtering for removing Artifacts: Random Noise, structured noise, and physiological interference; Stationary versus nonstationary processes, Noise in Event-related potentials, High –frequency noise in the ECG, Motion artifact in the ECG, Power-line interference in ECG signals, maternal interference in fetal ECG.

Time Domain Filters: Synchronized averaging, Moving –average filters, Derivative –based operators to remove low-frequency artifacts.

Unit 3 (11 Lectures)

Frequency –domain filters: Removal of high –frequency noise: Butterworth Lowpass filters, Removal of Low –frequency noise: Butterworth highpass filters, Removal of periodic artifacts: Notch and Comb filters; Optimal Filtering: The Wiener Filter

Adaptive Filters for removal of Interference: Adaptive noise canceller, Least-mean squares adaptive filter, recursive least –squares adaptive filter, selecting an appropriate filter, Application-Removal of artifacts in the ECG, Maternal-Fetal ECG.

Unit 4 (11 Lectures)

Event Detection: The P, Q,R,S, and T waves in the ECG, The first and second heart sounds, EEG Rhythms, waves and transients; Derivative based methods for QRS detection

Correlation Analysis of EEG channels: Detection of EEG rhythms, Template matching for EEG spike –and-wave detection; Cross-spectral techniques-coherence analysis of EEG channels, Detection of P wave, Homomorphic filtering-Generalized linear filtering, homomorphic deconvolution.

Text/Reference Books:

1. Rangaraj M. Rangayyan, "Biomedical Signal Analysis", John Wiley & Sons.
2. Willis J. Tompkins, "Biomedical Digital Signal Processing: C Language Examples and Laboratory Experiments for the IBM PC", Prentice Hall India.
3. Eugene N. Bruce, "Biomedical Signal Processing and Signal Modeling", John Wiley & Sons.
4. John L. Semmlow, "Biosignal and Biomedical Image Processing : MATLAB-Based Applications", CRC press.
5. S. Cerutti and C. Marchesi , "Advanced Methods of Biomedical Signal Processing", John Wiley & Sons.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand and analyze different types of biomedical signals like ENG, EMG, ECG, EEG etc which are useful for various disease diagnoses.
2. Identify artifacts in biomedical signals and using different types of filters to remove these.
3. Remove noise using suitable frequency domain filters like butterworth, optimum filter, and adaptive filters.
4. Detect different types of useful events in biomedical signals; analyze EEG channels, and use of homomorphic filtering.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:

Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines "AICTE Examination Reforms". Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE437C Fundamentals of Image Processing

B.Tech. 4th YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1(12 Lectures)

Introduction to Digital Image processing : Development of Digital Image processing, Components of an Image Processing System, Fundamental steps in Image Processing, Different Levels of Processing, Lower Level Processing Techniques, Applications of Image Processing.

Introduction to Image Acquisition: Capturing Devices, Light and the Electromagnetic Spectrum, Image Sensing and Acquisition: Image Acquisition using a single sensor, Image Acquisition using sensor strips, Image Acquisition using sensor arrays, A simple Image formation model.

Unit 2(12 Lectures)

Elements of Visual Perception: Structure of the Human Eye, Image Formation in Eye, Brightness Adaptation and Discrimination.

Image Digitization and Pixels: Basic Concepts in sampling and quantization, Representing Digital Images, Spatial and Intensity Resolution, Image Interpolation, Some Basic Relationships between pixels: Neighbours of a Pixel, Adjacency, Connectivity, Regions, and Boundaries, Distance Measures.

Unit 3(12 Lectures)

Image processing tools: Array versus Matrix Operations, Linear vs Nonlinear Operations, Arithmetic Operations, Set & Logical Operations, Spatial Operations, Vector and Matrix Operations, Probabilistic methods

Image Transforms & Image Registration: Unitary Transforms: Separable Unitary Transforms, Basis Images, Orthogonal Transforms, Basic Information Theory; Fourier Transform, Discrete Fourier Transform, Properties of Fourier Transform, Convolution & Correlation, Convolution in Frequency domain.

Unit 4(12 Lectures)

Image Enhancement in Spatial Domain: Pixel Grey Level Transformation: Linear & Nonlinear Transformations, Grey Level Slicing, Bit-Plane Slicing, Image Averaging; Mask Based Processing: Smoothing Linear filter- Geometric Mean Filter, Harmonic Mean Filter, Median Filter; Max & Min Filters, Sharpening Filters, Histogram Processing: Histogram Equalization, Contrast Stretching.

Image Enhancement in Frequency Domain: Basics of Filtering in the frequency domain: Frequency domain filtering fundamentals, Steps for frequency domain filtering, correspondence between filtering in the spatial and frequency domains, Image smoothing: Ideal Low pass filter, Butterworth Low pass Filter, Gaussian filter.

Text Books:

1. Rafael C. Gonzalez and Richard E. Woods, "Digital Image Processing", 3rd Edition, Pearson Education, 2016.
2. M. K. Pakhira, "Digital Image Processing and Pattern Recognition", PHI.

Reference Books:

1. Anil K Jain, "Fundamentals of Digital Image Processing", Pearson Education, 2015.
2. Keenneth R Castleman, " Digital Image Processing", Pearson Education.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand, represent, and analyse analog & digital images mathematically.
2. Interpret image quality, image acquisition, image resolution, and human visual perception.
3. Represent and analyse the images using various transforms to be used for different applications and image registration.
4. Choose processing techniques as per application in spatial and frequency domain and interpret the results; applying the image processing concepts practically.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE439C Satellite Communication

B.Tech. 4th YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits
3	0	0	3

Class Work	: 25
Examination	: 75
Total	: 100
Duration of Exam	: 3 Hours

Unit 1 (9 Lectures)

Introduction: Introduction to Satellite Communication: Principles and architecture of satellite Communication, Brief history of Satellite systems, advantages, disadvantages, applications and frequency bands used for satellite communication. Applications of satellite communication. Future trends in satellite communications.

Satellite link budget: Flux density and received signal power equations, Calculation of System noise temperature for satellite receiver, noise power calculation, earth station parameters.

Unit 2 (10 Lectures)

Orbital Mechanics: Orbital equations, Kepler's laws, Apogee and Perigee for an elliptical orbit, evaluation of velocity, orbital period, angular velocity etc. of a satellite, concepts of Solar day and Sidereal day.

Satellite sub-systems: Study of Architecture and Roles of various sub-systems of a satellite system such as Telemetry, tracking, command and monitoring (TTC & M), Attitude and orbit control system (AOCS), Communication sub-system, power sub-systems etc. Typical Phenomena in Satellite Communication: Solar Eclipse on satellite, its effects, remedies for Eclipse, Sun Transit Outage phenomena, its effects and remedies, Doppler frequency shift phenomena and expression for Doppler shift.

Unit 3 (10 Lectures)

Modulation and Multiple Access Schemes: Various modulation schemes used in satellite communication, Meaning of Multiple Access, Multiple access schemes based on time, frequency, and code sharing namely TDMA, FDMA and CDMA. Hybrid Access techniques.

Unit 4 (12 Lectures)

Laser Satellite Communication: Link analysis, optical satellite link transmitter, optical satellite link receiver, satellite beam acquisition, tracking & positioning, deep space optical communication link.

Special Purpose Communication Satellite: Satellite for earth observation, satellite for weather forecast, satellite for scientific studies, satellite for military applications, satellite television, telephone services via satellite, data communication services, VSAT, MSAT, GPS systems.

Text/Reference Books:

1. Timothy Pratt Charles W. Bostian, Jeremy E. Allnut: Satellite Communications: Wiley India. 2nd edition 2002
2. Tri T. Ha: Digital Satellite Communications: Tata McGraw Hill, 2009
3. Dennis Roddy: Satellite Communication: 4th Edition, McGraw Hill, 2009
4. Wilbur L Pritchard, Henri G, Suyderhoud and Robert A. Nelson: Satellite Communication System Engineering, 2nd edition Pearson Education.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand the basic concepts of satellite communication and utilize their concepts in designing of various requirements and upgradation in the field of satellite communication.
2. Get acquainted with telemetry, tracking, command and monitoring models along with link designing in the field of satellite communication.
3. Acquire in-depth knowledge of earth station technology and will also review various digital modulation techniques.
4. Gain in-depths into the state-of-the-art development in specialized and purpose satellite along various multiple access techniques employed in advanced communication system which helps in predicting weather, natural disaster and will help mankind to take preventive action according.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE441C Artificial Neural Networks & Fuzzy Logic

B.Tech. 4th YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits
3	0	0	3

Class Work	: 25
Examination	: 75
Total	: 100
Duration of Exam	: 3 Hours

Unit 1(12 Lectures)

Neuro-Fuzzy Techniques:Neuro-fuzzy techniques, need of neuro-fuzzy techniques, Neural and fuzzy intelligence, fuzziness vs Multivalence: the dynamical systems to machine intelligence.

Neural Networks: Neural networks characteristics, difference between biological neuron & A.N.N, History of development in neural networks principles, Artificial neural net terminology, Model of a neuron, Topology.

Unit 2(10 Lectures)

Neural Networks Models & Rules:Feed forward network, feedback network, Supervised, Unsupervised, Re-enforcement learning. Knowledge, representation and acquisition. Basic Hop field model, Types of learning, Hebbian learning, Perception learning, Delta learning, Window–Hoff Learning correlation Learning, Winner-Take–all learning rule.

Artificial Neural Networks & Applications:Radial basis function neural networks, Basic learning laws in RBF nets, Recurrent back propagation, Introduction to counter propagation networks, ART networks, Associative Memories, Applications such as pattern recognition, speech and decision-making.

Unit 3(10 Lectures)

Fuzzy Logic: Basic concepts of fuzzy logic, Fuzzy vs. Crisp set, Linguistic variables, Membership functions, Fuzzy sets & Operations of fuzzy sets, Fuzzy IF- THEN rules.

Fuzzy System:Fuzzy system, Fuzzy Inference System, De-Fuzzification.

Unit 4(11 Lectures)

Applications Of Fuzzy Logic: Application of Fuzzy logic: Industrial automation, energy saving AC control, washing machines, automatic target tracking, ABS system, Traffic light controller.

Neuro-Fuzzy System: Introduction, combining fuzzy system with neural network, properties of Neuro-Fuzzy system, Neuro-Fuzzy architecture, applications.

Text/Reference Books:

1. B. Yegnanarayana, " Artificial Neural Networks"PHI.
2. J.M. Zurada, "Introduction to artificial neural systems", Jaico Pub.
3. ROSS J.T , "Fuzzy logic with engineering application", TMH.
4. Simon Haykin, "Neural Networks", PHI.
5. Ahmad M.Ibrahim, "Introduction to applied Fuzzy Electronics", (PHI).
6. P.D. wasserman, "Neural computing theory & practice", (ANZA PUB).

Course Outcome: At the end of this course, student will demonstrate the ability to:

1. Understand Neuro-Fuzzy techniques and basics of neural network with respect to biological neural networks.
2. Understand and differentiate between various neural network architectures which will further help the students to implement real time applications i.e. pattern recognition, speech recognition etc. which in turn helps in connection of speech related human problems.
3. Understand fuzzy logics, rules and systems for solving day to day problems prevailing our society.
4. Understand fuzzification with neural networks and to understand applications of fuzzy systems i.e. ABS system, Target tracking, Washing Machines etc. which automatically improves the living standards of a common man.

Note:

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE443C ASICs and FPGAs
B.Tech. 4th YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (9 Lectures)

Introduction of ASICs:Types of ASICs - Design flow – CAD tools used in ASIC Design – Programming ASICs:Antifuse – static RAM – EPROM and EEPROM based ASICs, Programmable Logic Devices : ROMs and EPROMs – PLA –PAL. Gate Arrays – CPLDs and FPGAs

Unit 2 (12 Lectures)

ASIC Physical design: System partition -partitioning - partitioning methods – interconnect delay models and measurement of delay - floor planning - placement – Routing : global routing - detailed routing - special routing - circuit extraction – DRC

Unit 3 (12 Lectures)

FPGA:Field Programmable gate arrays- Logic blocks, routing architecture, Design flow technology - mapping for FPGAs, Xilinx XC4000 - ALTERA's FLEX 8000/10000, ACTEL's ACT-1,2,3 and their speed performance Case studies: Altera MAX 5000 and 7000 - Altera MAX 9000 – Spartan II and Virtex II FPGAs - Apex and Cyclone FPGAs

Unit 4 (9 Lectures)

SOCs:System-On-Chip Design - SoC Design Flow, Platform-based and IP based SoC Designs, Basic Concepts of Bus-Based Communication Architectures, On-Chip Communication Architecture Standards, Low-Power SoC Design

Text/Reference Books:

1. M.J.S .Smith, "Application Specific Integrated Circuits, Addison -Wesley Longman Inc.,1997.
2. Wayne Wolf, FPGA-Based System Design, Prentice Hall PTR, 2004.
3. S. Trimberger, Field Programmable Gate Array Technology, Edr, Kluwer Academic Publications, 1994.
4. John V.Oldfield, Richard C Dore, Field Programmable Gate Arrays, Wiley Publications1995.
5. P.K.Chan& S. Mourad, Digital Design Using Field Programmable Gate Array, PrenticeHall, 1994.
6. Parag.K.Lala, Digital System Design using Programmable Logic Devices , BSP, 2003.
7. S. Brown, R. Francis, J. Rose, Z. Vransic, Field Programmable Gate Array, Kluwer Pubin,1992.
8. J. Old Field, R.Dorf, Field Programmable Gate Arrays, John Wiley & Sons, Newyork,1995.
9. FarzadNekoogar and FaranakNekoogar, From ASICs to SOCs: A Practical Approach,Prentice Hall PTR, 2003.
10. R. Rajsuman, System-on-a-Chip Design and Test. Santa Clara, CA: Artech House Publishers, 2000.
11. F. Nekoogar. Timing Verification of Application-Specific Integrated Circuits (ASICs). Prentice Hall PTR, 1999.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand the design flow related to ASICs and FPGA.
2. Learn the standard tools that can be used to develop various hardware applications.
3. Learn and implement industry standard bus architectures.
4. Understand the complete architectures and their applications

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines "AICTE Examination Reforms". Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE483C Project Stage-I

B.Tech. 4th YEAR (SEMESTER –VII) Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
0	0	8	4	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

The project started in VII Semester will be completed in VIII Semester (***applicable for those students only who will not opt for internship in VIII Semester**) and will be evaluated through a panel of examiners consisting of the following:

Head/ Chairperson of Department :	Chairperson
Project coordinator :	Member
External examiner :	To be appointed by the University

The student will be required to submit two copies of his/her project report to the department for record (one copy each for the department and participating teacher).

Project coordinator will be assigned the project load of, maximum of 2 hrs. per week including his own guiding load of one hr. However, the guiding teacher will be assigned maximum of one period of teaching load irrespective of number of students/groups under him/her.

The format of the cover page and the organization of the body of the report for all the B.Tech. will be finalized and circulated by the Dean, Faculty of Engineering and Technology.

Course Outcomes: After completing this course, the student will have:

1. Practical exposure of Industrial Projects Skill
2. Learn various aspects of software and hardware handling of industrial work.
3. Attitude more professionally inclined.
4. Better understanding about time management.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE485C Professional Training (Level-3)

B.Tech. 4th YEAR (SEMESTER –VII) Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 100
0	0	2	2	Total	:100
				Duration of Exam	: 3 Hours

At the end of 6th semester each student would undergo four weeks Professional Training in an Industry/ Institute/ Professional Organization/Research Laboratory etc. with the prior approval of the Training and Placement Officer of the University and submit in the department a typed report along with a certificate from the organization.

The typed report should be in a prescribed format.

The report will be evaluated in the 7th Semester by a Committee consisting of three teachers from different specialization to be constituted by the Chairperson of the department. The basis of evaluation will primarily be the knowledge and exposure of the student towards different processes and the functioning of the organization.

The student will interact with the committee through presentation to demonstrate his/her learning.

Teachers associated with evaluation work will be assigned 2 periods per week load.

COURSE OUTCOMES: After the course is completed the student will have:

1. Additional knowledge about professional attributes.
2. The students will develop a more professional outlook.
3. The students will know how to deal with time bound tasks in a more effective way.
4. The students will have more efficient attribute of multi-tasking.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

CSE 305C Computer Networks
B.Tech. 3rd YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (10 Lectures)

OSI Reference Model and Network Architecture: Introduction to Computer Networks, Example Networks ARPANET, Internet, Private Networks, and Network Topologies: Bus, Star, Ring, Hybrid, Tree, Complete, Irregular –Topology; Types of Networks: Local Area Networks, Metropolitan Area Networks, Wide Area Networks; layering architecture of networks, OSI model, Functions of each layer, Services and Protocols of each layer.

Unit 2 (10 Lectures)

TCP/IP: Introduction, History of TCP/IP, Layers of TCP/IP, Protocols, Internet Protocol, Transmission Control Protocol, User Datagram Protocol, IP Addressing, IP address classes, Subnet Addressing, Internet Control Protocols, ARP, RARP, ICMP, Application Layer, Domain Name System, Email – SMTP, POP, IMAP; FTP, NNTP, HTTP, Overview of IP version 6.

Unit 3 (10 Lectures)

Local Area Networks: Introduction to LANs, Features of LANs, Components of LANs, Usage of LANs, LAN Standards, IEEE 802 standards, Channel Access Methods, Aloha, CSMA, CSMA/CD, Token Passing, Ethernet, Layer 2 & 3 switching, Fast Ethernet and Gigabit Ethernet, Token Ring, LAN interconnecting devices: Hubs, Switches, Bridges, Routers, Gateways.

Unit 4 (10 Lectures)

Wide Area Networks: Introduction of WANs, Routing, Congestion Control, WAN Technologies, Distributed Queue Dual Bus (DQDB), Synchronous Digital Hierarchy (SDH)/ Synchronous Optical Network (SONET), Asynchronous Transfer Mode (ATM), Frame Relay, Wireless Links.

Introduction to Network Management: Management, Class of Service, Quality Firewalls, VLANs, Proxy Servers. Remote Monitoring Techniques: Polling, Traps, Performance of Service, Security management, Digital signatures, SSL.

Text/Reference Books:

1. Computer Networks (3rd edition), Tanenbaum Andrew S., International edition, 1996.
2. Data Communications, Computer Networks and Open Systems (4th edition), Halsall Fred, 2000, Addison Wesley, Low Price Edition.
3. Business Data Communications, Fitzgerald Jerry, Computer Networks – A System Approach, Larry L. Peterson & Bruce S. Davie, 2nd Edition.

Course Outcomes: After completing the course, student will demonstrate the ability to:

1. To understand the organization of computer networks, factors influencing computer network development and the reasons for having variety of different types of networks.
2. To apply knowledge of different techniques of error detection and correction to detect and solve error bit during data transmission.
3. To design a network routing for IP networks.
4. To demonstrate proper placement of different layers of ISO model and illuminate its function and determine proper usage of the IP address, subnet mask and default gateway in a routed network.

Note:

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

CSE 431C Cyber Security
B.Tech. 3rd YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (10 Lectures)

Introduction To Cybercrime:- Cybercrime and Information Security, Classificationsof Cybercrimes, The need for Cyber laws, The Indian IT Act Challenges to Indian Law and Cybercrime Scenario in India, Weakness in Information Technology Act and it consequences, Digital Signatures and the Indian IT Act, Cybercrime and Punishment; Technology, Students and Cyber law; Survival tactics for the Netizens, Cyber-offenses: Cybers talking, Cyber cafe and Cyber crimes, Botnets, Attack Vector, Cloud Computing.

Unit 2 (10 Lectures)

Tools And Methods Used In Cybercrime:- Proxy Servers and Anonymizers, Phishingand identity theft, Password Cracking, Keyloggers and Spywares, Virus and Worms, Trojan Horses and Backdoors, Stenography, DoS and DDoS Attacks, SQL Injection, Buffer Overflow; Cybercrime: Mobile and Wireless Devices: Trends in Mobility, Attacks on Wireless Networks, Credit Card Frauds in Mobile and Wireless Computing Era, Security Challenges, Registry Settings for Mobile Devices, Authentication Service Security Attacks on Mobile/Cell Phones.

Unit 3 (10 Lectures)

Understanding Computer Forensics:- The Need for Computer Forensics, Cyberforensics and Digital Evidence, Forensics Analysis of E-Mail, Digital Forensics Life Cycle, Chain of Custody Concept, Network Forensics, Computer Forensics and Stenography, Relevance of the OSI 7 Layer Model to Computer Forensics, Forensics and Social Networking Sites: The Security/Privacy Threats, Challenges in Computer Forensics, Forensics Auditing, Anti forensics.

Unit 4 (10 Lectures)

Cyber security Organizational Implications:- Cost of Cybercrimes and IPR Issues,Web Threats for Organizations, Security and Privacy Implications from Cloud Computing, Social Media Marketing, Social Computing and the Associated Challenges for Organizations, Protecting People's Privacy in the Organization, Organizational Guidelines for Internet Usage, Safe Computing Guidelines and Computer Usage Policy, Incident Handling, Forensics Best Practices, Media and Asset Protection, Importance of Endpoint Security in Organizations.

Text/Reference Books:

1. “Cyber Security Understanding Cyber Crimes, Computer Forensics and Legal Perspectives”, Nina Godbole, Sunit Belapur, Wiley India Publications, April, 2011.

Course Outcomes: After completing the course, student will demonstrate the ability to:

1. Able to demonstrate cyber crime its laws and related terms.
2. Work with SQL injection, DOS attacks etc.
3. Explain computer forensic, Network forensic cyber forensic.
4. Understand safe computing guidelines, usage policies and incident handling.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

CHE457C Industrial Safety
B.Tech. 4th YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (10 Lectures)

Introduction: Concept of loss prevention, origin of process hazards, types of process hazards, acceptable risks, accident and loss statistics, nature of accident process, concepts of inherent safety in plants or Factories, dose Vs response curve, toxicants entry route, thresh limit values, safety regulations.

Unit 2 (10 Lectures)

Hazards: Fire, Chemical (industrial and laboratory scale), electrical, mechanical, biohazards (natural and anthropogenic), toxic materials, their types and preventive measures, Liquid and vapor phase hazardous methods, storage and handling, containment, precautions, Personal safety precautions.

Unit 3 (10 Lectures)

Risk management principles, risk analysis techniques, risk control, hazards operability studies, hazard analysis, Fault tree analysis, Consequences analysis, human error analysis, accidental error analysis, economics of risk management, check list, reliability theory, event tree, HAZOP, safety reviews, what if analysis.

Unit 4 (10 Lectures)

Safety audit, procedure for safety auditing, audit report, safety report, safety training, emergency planning and disaster management, introduction to security risk factors tables.

Text Books:

1. Chemical Hazards and safety, 2nd Edition, DawandeDenet& Co. , 2012
2. Loss preventions in process industries, Lees Butterworth-Heinemann, 1980.
3. Industrial safety Handbook, William and Handley, McGraw Hill.

Reference Books:

1. Safety and Hazard management in Chemical Industries, Vyas, Atlantic 2013.
2. Industrial safety, health environment & Security, Basudev Panda, Laxmi publication ISBN- 97893-81159-43-9.
3. Industrial Safety and Health Management, 4th Edition, C. Ray Asfahl, Prentice Hall International Series, 1984.
4. Industrial Accident Prevention : A Safety Management Approach, Herbert William Heinrich.

Course Outcomes: After completing the course, student will demonstrate the ability to:

1. Analyze the effect of release of toxic substances.
2. Understand the industrial laws, regulations and source models.
3. Understand the methods of hazard identification and preventive measures and develop safety programs to prevent the damage or loss.
4. Conduct safety audits and improve safety practices

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

CE406C Disaster Management

B.Tech. 4th YEAR (SEMESTER –VII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (10 Lectures)

Introduction to Disaster Management: Disaster, Emergency, Hazard, Mitigation, Disaster Prevention, Preparedness and Rehabilitation, Risk and Vulnerability, Classification of Disaster, Natural and Man-made Disasters, International day and Decade of Disaster Reduction.

Risk and Vulnerability to disaster mitigation and management options: Warning and Forecasting.

Unit 2 (12 Lectures)

Hydro-meteorological based disasters I: Disaster Management Act 2005, Role of NDMA, NDRF, NIDM, Tropical Cyclones, Floods, droughts, mechanism, causes, role of Indian Metrological Department, Central Water Commission, structure and their impacts, classifications, vulnerability, Early Warning System, Forecasting, Flood Warning System, Drought Indicators, recurrence and declaration, Structural and Non-structural Measures.

Hydro-meteorological based disasters II: Desertification Zones, causes and impacts of desertification, Characteristics, Vulnerability to India and Steps taken to combat desertification, Forest Fires; Causes of Forest Fires; Impact of Forest Fires, Prevention.

Unit 3 (10 Lectures)

Geological based disasters: Earthquake, Reasons, Compression, Shear, Rayleigh and Love Waves; Magnitude and Intensity Scales, Direct and Indirect Impact of Earthquake; Seismic Zones in India, Factors, Indian Standards Guidelines for RCC and Masonry Structures, Prevention and Preparedness for Earthquake, Tsunamis, Landslides and avalanches: Definition, causes and structure; past lesson learnt and measures taken; their Characteristic features, Impact and prevention, Atlas (BMTRPC); structural and non-structural measures.

Unit 4 (10 Lectures)

Manmade Disasters I: Chemical Industrial hazards; causes and factors, pre- and post-disaster measures; control; Indian Standard Guidelines and Compliance;

Traffic accidents; classification and impact, Fire hazards; Classification as per Indian Standards;

Fire risk assessment; Escape routes; fire-fighting equipment; classification of buildings, fire zones, occupancy loads; capacity and arrangements of exits,

Use of remote sensing and GIS in disaster mitigation and management.

Text Books:

1. Thomas D. Schneid , Disaster Management and Preparedness, CRC Publication, USA, 2001.
2. Patrick Leon Abbott, Natural Disasters, Amazon Publications, 2002.
3. Ben Wisner., At Risk: Natural Hazards, People vulnerability and Disaster, Amazon Publications, 2001.
4. Oosterom, Petervan, Zlatanova, Siyka, Fendel, Elfriede M., “Geo-information for Disaster Management”, Springer Publications, 2005.
5. Savindra Singh and Jeetendra Singh, Disaster Management, Pravalika Publications, Allahabad.
6. Nidhi Gauba Dhawan and Ambrina Sardar Khan, Disaster Management and Preparedness, CBS Publishers & Distribution.

Reference Books:

1. Selected Resources Published by the National Disaster Management Institute of Home Affairs, Govt. of India, New Delhi.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

Course Outcomes: After completing the course, student will demonstrate the ability to:

1. Understand the significance of disaster management.
2. Analyze the occurrences, reasons and mechanism of various types of disaster.
3. Understand the preventive measures as Civil Engineer with latest codal provisions.
4. Apply the latest technology in mitigation of disasters.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE402C Information Theory and Coding

B.Tech 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (9 Lectures)

Field Algebra: Basics of information theory, Information source , binary source, discrete & memoryless channel , channel capacity , source coding theory , Average information or entropy ,Mutual information, Field Algebra: binary operations, groups field, vector space, Galois field, Matrices.

Unit 2 (10 Lectures)

Block codes: Shannon fano coding, Huffman coding, arithmetic coding, run length coding, Block Codes: linear block code, convolution code, cyclic code, BCH code, Trellis code modulation, Syndrome, run length coding, generator & parity check matrices.

Unit 3 (12 Lectures)

Modulation & tradeoffs: Modulation and coding tradeoffs :Goals of communication system designer, error probability plane, Nyquist minimum bandwidth, Shannon Hartley capacity theorem, bandwidth efficiency plane, modulation and coding tradeoffs, designing and evaluating digital communication systems, bandwidth efficient modulation, modulation and coding for band limited channels, trellis coded modulation Performance of codes: Performance of linear block codes & convolution codes, Bounds on code performance, Bounds on error performance.

Unit 4 (12 Lectures)

Channel Coding & source coding: Waveform Coding, types of error control, structured sequences, error detecting and correcting capability, usefulness of standard array, interleaving and concatenated codes, coding and interleaving applied to the compact disc. Source Coding: Sources, amplitude quantizing, adaptive prediction, transform coding, source coding for digital data, examples of source coding.

Text/Reference Books:

1. N. Abramson, Information and Coding, McGraw Hill, 1963.
2. M. Mansurpur, Introduction to Information Theory, McGraw Hill, 1987.
3. Arijit Saha, Information Theory Coding & Cryptography, Pearson Education.
4. Shu Lin and D.J. Costello Jr., Error Control Coding, Pearson Education.
5. Bernard Skylark & Pabitra Kumar Ray, Digital communications Fundamentals and Applications, Pearson Education.
6. Ranjan Bose, information theory ,coding and cryptography third edition , Mc Graw Hill

Course Outcomes: After completing the course, student will be able to:

1. Understand the concept of information and entropy
2. Understand Shannon's theorem for coding
3. Calculation of channel capacity
4. Apply coding techniques

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:

Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines "AICTE Examination Reforms". Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE422C Radar & Sonar Engineering

B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1(10 Lectures)

Introduction To Radar: Radar Block Diagram & operation, Radar Frequencies, Radar development, Application of Radar.

Radar Equation: Simple form of Radar Equation, Prediction of Range performance, Minimum detectable signal, Receiver noise, Signal to Noise ratio, Transmitter Power, Pulse repetition frequency & range ambiguities, System losses, Propagation effects.

Unit 2(12 Lectures)

CW & Frequency Modulated Radar: The Doppler Effect, CW Radar, Frequency-modulated CW Radar, Multiple Frequency CW Radar.

MTI & Pulse Doppler Radar: Introduction, Delay Line Cancellors, Multiple or staggered, Pulse repetition frequencies, range-Gated Doppler Filters, Digital Signal Processing, Other MTI delay line, Limitation of MTI performance, Noncoherent MTI, Pulse Doppler Radar, MTI from a moving platform.

Unit 3(10 Lectures)

Tracking Radar: Tracking with Radar, Sequential Lobbing, Conical Scan, Monopulse Tracking Radar, Tracking in range, Acquisition.

Receivers, Displays & Duplexers: Radar Receivers, Noise Figure, Mixer, Low-noise Front ends, Displays, Duplexer, Receiver protectors.

Unit 4(12 Lectures)

Introduction To Sonar: History of sonar, under water propagation: sound velocity profile, propagation mode, multipaths; Types of sonar system: active, generic active and passive.

Sonar Parameters: Basic Types of noise in sonar system, Detection of acoustic energy using sonar: detection criterion, sonar system performance, figure of merit; Sonar transducers.

Text Books:

1. Introduction to Radar Systems: Merrill I. Skolnik, ; MGH

Reference Books:

1. Electronic Communication Systems : Kennedy; TMH
2. Fundamental of DADAR, SONAR and Navigation Engineering: K.K. Sharma; Katson Books

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand the basic concept of Radar Engineering and will be design mathematical model of RADAR for Global application such as automotive communication etc. in turn adds to concepts of smart surveillance.
2. Design modern RADAR such as FMCW, MTI RADAR, Pulse Doppler RADAR etc and will understand limitation, also improve the exciting design results in detection.
3. Understand tracking RADARS, RADAR Receivers and design its noise models.
4. Learn basic principal of Sonar system and apply detection & estimation theory on sonar problems .

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:

Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE424C Information Security
B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L T P Credits
3 0 0 3

Class Work : 25
Examination : 75
Total : 100
Duration of Exam : 3 Hours

Unit 1 (10 Lectures)

Introduction to information systems: Types of information Systems, Development of Information Systems, Introduction to information security, Need for Information security, Threats to Information Systems, Information Assurance, Cyber Security, and Security Risk Analysis.

Unit 2 (12 Lectures)

Application security: (Database, E-mail and Internet), Data Security Considerations-Backups, Archival Storage and Disposal of Data, Security Technology-Firewall and VPNs, Intrusion Detection, Access Control.

Security Threats: Viruses, Worms, Trojan Horse, Bombs, Trapdoors, Spoofs, E-mail viruses, Macro viruses, Malicious Software, Network and Denial of Services Attack, Elementary Cryptography: Substitution Ciphers, Transpositions, The Data Encryption Standard, The AES Encryption Algorithms, Public Key Encryptions, Uses of Encryption.

Unit 3 (9 Lectures)

Data base Security: Security requirements, Reliability and integrity, Sensitive data, Inference, multilevel database, proposals for multilevel security.

Security in Network: Threats in Network, Network Security Controls, Firewalls, Intrusion Detection Systems, Secure E-Mail.

Unit 4 (10 Lectures)

Security Policies, Standards, Procedure and Guidelines: Why Policies should be developed, WWW policies, Email Security policies, Policy Review Process-Corporate policies-Sample Security Policies, Publishing and Notification Requirement of the Policies.

Information Security Standards: ISO, IT Act, Copyright Act, Patent Law, IPR. Cyber Laws in India; IT Act 2000 Provisions, Intellectual Property Law: Copy Right Law, Software License, Semiconductor Law and Patent Law.

Text/Reference Books:

1. Mark Stamp: Information Security Principles and Practices, 2nd edition, John Wiley & Sons, Inc., 2005.
2. V.K. Pachghare, Cryptography and information Security, PHI Learning Private Limited, Delhi India.
3. Chander, Harish, Cyber Laws And It Protection , PHI Learning Private Limited ,Delhi ,India.
4. Surya Prakash Tripathi, Ritendra Goyal, Praveen Kumar Shukla ,Introduction to Information Security and Cyber Law, Willey Dreamtech Press.
5. Charles P. Pfleeger, Shari Lawrence Pfleeger, Security in Computing, 3rd Edition. PHI.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand the importance of information security.
2. Understand the basic concept of cryptography and its applications.
3. Understand security requirements for data base security and network security.
4. Understand security policies standards and guidelines.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:

Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE426C Telecommunication Switching Systems

B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1(12 Lectures)

Evolution Of Switching System:What is Switching, Types of Switching, Block Diagram of Telecommunication Network, Switching System Fundamentals, Classification of Switching System, Elements of a Switching System, Basic Function of Switching System, Basic Telephone Communication, Function of a Manual Switching System, Magneto or Local Battery Switchboard, Common Battery Switchboard, Limitations of Manual Switching System, Introduction to strowger switching system.

Crossbar Switching System: Introduction, Principle of Common Control, Touch Tone Dial Telephone, Crossbar Switch Mechanism, Principle of Crossbar Switching, Crossbar Switch Configurations, Organisation of a Crossbar Telephone Switch, A General Trunking, Electronic Switching, Classification Crosspoint Technology

Unit 2(12 Lectures)

Space Division Switching:Stored Program control, Centralised SPC, Distributed SPC, Software Architecture, Application software, Enhanced Services, Two Stage Networks, n-Stage Networks.

Time Division Switching: Introduction, Analog Time Division Switching, Digital Time Division Switching, A Digital Memory Switch, Time Stages in General, Two-Dimensional Switching, Multiple Stage Time and Space Switching

Unit 3(10 Lectures)

Packet Switching:Statistical Multiplexing, Local area & wide area networks, Large Scale Networks, Broadband Networks

Teletraffic Engineering:Introduction, Network Traffic Load, CCITT Recommended Busy Hours, Traffic Terminology, The Unit of Traffic, Congestion, Grade of Service, Blocking Probability, Traffic Measurements, Modelling Switching System, Markov processes representing traffic. Calculation of blocking probability, stationary probability measures for Ergodic Markov processes. Combinatorial interpretation, calculation of blocking probability.

Unit 4(10 Lectures)

Control Of Switching Systems: Call Processing functions, common control, Reliability, Availability & Security.

Signalling:Customer Line Signalling, Audio frequency junctions & trunk circuits, FDM carrier Systems, PCM signalling, Inter – register signalling, Common channel Signalling Principles.

Text Books:

1. Thiagarajan Viswanathan, “Telecommunication Switching Systems and Networks”, PHI
2. Syed Riffat Ali, “Digital switching Systems, system reliability and analysis”, Tata MC Graw, 2002.
3. Flood, Telecommunication Switching, Traffic and Networks, Pearson Education.

Reference Books:

1. Keshav S, “An Engineering Approach to Computer Network Networking”, Pearson Education.
2. Martin, “Telecommunication & Computer 3e”, PHI

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Describes the need for switching system and their solution from analogue to digital and describes the PSTN, Private Network and Integrated Network.
2. To compare telephone Network, date network, ISDN Network and analyze Transmitter and receiver circuit.
3. Compare and contest design issues advantages disadvantages and limitation of analog system and understand basic of telecommunication networking and information technologies.
4. Continuously improve their technology knowledge and communication Skill.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE428C Digital Image Processing for Medical Applications

B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (11 Lectures)

Introduction: Imaging systems, objects and images, the digital image processing system, applications of digital image processing.

Imaging Systems: The human visual pathway: brightness response and spatial resolution of the eye; Photographic film: response of film to light, spatial resolution of film; other sensors, Digitizing an image: spatial quantization, intensity quantization, Quality of a digital image: pixel size, spatial and brightness resolution, noise content; color images.

Unit 2 (11 Lectures)

Medical images obtained with ionizing radiation: medical imaging modalities, images from x-rays: Plain and Computed radiography, mammography, Fluoroscopy and digital subtraction angiography, computed tomography, images from γ -rays: Planer scintigraphy, SPECT imaging, PET imaging, Dose and risk.

Unit 3 (12 Lectures)

Medical images obtained with non-ionizing radiation: Ultrasound imaging: image Quality, Doppler imaging, clinical applications of ultrasound; Magnetic resonance imaging: nuclear magnetic resonance, MRI and pulse sequencing, picture archiving and communication systems: multimodal registration.

Unit 4 (12 Lectures)

Grey level histogram: Dynamic range and contrast, Entropy, signal to noise ratio, other histogram features; Histogram transformation and look up tables: Histogram stretch, histogram equalization, Histogram matching, local histogram transformations, other histogram transformations

Spatial domain image enhancement: image averaging, image subtraction, multiplication and division, logical operations.

Text/Reference Books:

1. G. Dougherty, "Digital Image Processing for medical applications", Cambridge University Press.
2. Rafael C. Gonzalez and Richard E. Woods, "Digital Image Processing", 3rd Edition, Pearson Education, 2016.
3. M. K. Pakhira, "Digital Image Processing and Pattern Recognition", PHI.
4. Anil K Jain, "Fundamentals of Digital Image Processing", Pearson Education, 2015.
5. Keenneth R Castleman, "Digital Image Processing", Pearson Education.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand imaging and its applications in medical. How human visual system capture and analyse the images.
2. Understand the acquisition of medical images using ionized radiation, like x-rays and γ -rays.
3. Understand medical images using non ionized radiation like ultrasound and MRI, and their applications.
4. Interpret and enhance images by histogram processing and other spatial domain techniques.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines "AICTE Examination Reforms". Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE430C Reliability Engineering
B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1(12 Lectures)

Introduction: Definition for Reliability, Need for reliability engineering, Causes of failures, catastrophic failures and degradation failures. Characteristic types of failures, useful life of components, Exponential case of chance failures, Reliability measures, Derivation for exponential distribution function, other kinds of distributions, Binomial, Poisson uniform, Rayleigh, Weibull, Gamma distributions, Markov chains, failures data analysis.

Reliability in Systems: Reliability Block Diagrams, series systems, parallel systems, K-out of - M systems, Open and short circuit failures, standby systems. Reliability Analysis of Non-series Parallel system, Boolean Algebra Method, Cut-set approach, delta star method, logical signal relation method, Bayes Theorem Method

Unit 2(10 Lectures)

Reliability Prediction: Objective of reliability Prediction, Classification, information sources for failure rate data, prediction methodologies, general requirement, role and limitations of reliability prediction

Reliability Allocation: Subsystems reliability improvement, Apportionment for new units, criticality.

Unit 3(10 Lectures)

Redundancy Techniques for reliability: Forms of maintenance, measures of maintainability and availability, maintainability function, availability function, two unit parallel system with repair, Markov model for two unit systems, preventive maintenance, provisioning of spares.

Reliability Testing: Kinds of testing, component reliability measurements parametric methods, confidence limits, accelerate testing, equipment acceptance testing.

Unit 4(10 Lectures)

Economics of Reliability Engineering: Reliability cost, effect of reliability on cost. Reliability achievement cost models, reliability utility cost models, replacement policies.

Integrated performance measures for communication systems: Integration of reliability and capacity, Delay related reliability.

Text Books:

1. K.K Aggarwal, “Reliability Engineering”, Kluwer Academic Netherlands.
2. B Singh, “Quality Control and Reliability Analysis”, Khanna Publishers.
3. Singiresu S. Rao, Reliability Engineering, Pearson Education, 2016.

Reference Books:

1. KB Mishra “ Reliability Prediction & Analysis: A Methodology oriented treatment” ,Elsevier,Netherlands.
2. Ebeling, “Introduction to Reliability & Maintainability”, TMH

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Apply the reliability concepts to the real time systems at their job place.
2. Improve the existing designs by using various reliability models so as to improve the efficiency and comfort while working.
3. Understand the concept & importance of preventive maintenance as compared to corrective maintenance which further improves the life of machines/systems.
4. Reduce the cost of a particular system which will reduce the financial liability on individual’s pocket.

Note:

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE332C PLC & SCADA Systems
B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits
3	0	0	3

Class Work	: 25
Examination	: 75
Total	: 100
Duration of Exam	: 3 Hours

Unit 1 (10 Lectures)

Programmable Logic Controller (PLC) Basics: Introduction, Parts of PLC, Principles of operation, PLC size and applications, PLC advantages and disadvantages, PLC manufacturers, PLC hardware components, I/O section, Analog I/O modules, Digital I/O modules, CPU- Processor memory module, Programming devices, Devices which can be connected to I/O modules, Relay, Contactor, SPST, Push Buttons, NO/NC Concept.

Unit 2 (11 Lectures)

Programming of PLC: General PLC Programming Procedures, Contacts and Coils, Programming Languages, Ladder Programming, Relay Instructions, Instruction Addressing, Concept of Latching, Branch Instructions, Contact and Coil I/O Programming Examples, Relation of Digital Gate Logic to Contact/Coil Logic.

Unit 3 (12 Lectures)

PLC Functions: Timer Instructions: ON DEAY Timer and OFF DELAY timer, Counter Instructions: UP/DOWN Counters, Timer and Counter Applications, Program Control Instructions: Master Control Reset, Jump and Subroutine, Math Instructions: ADD, SUB. Data Handling: Data Move, Data Compare, Data Selection.

Unit 4 (9 Lectures)

SCADA: Introduction of SCADA, Need of SCADA, advantages and disadvantages of SCADA, Elements of SCADA System, Real Time Systems, Remote Control, SCADA communications, Brief introduction to RTU and MTU, Sensors, actuators and wirings, Applications of SCADA in Smart Grid.

Text Books:

1. Frank D. Petruzella: Programmable Logic Controllers, McGraw-Hill Book Company.
2. Stuart A. Boyer : Supervisors Control and Data Acquisition, 3rd Edition, ISA
3. John W. Webb and Ronald A. Reis: Programmable Logic Controllers: Principles and Applications, 5th Edition Pearson.

Reference Books:

1. William I. Fletcher: An Engineering Approach to Digital Design, Pearson.
2. Colin D. Simpson: Programmable Logic Controllers, Pearson
3. Gray Dunning : Introduction to Programmable Logic Controllers, Delmar Thompson Learning
4. John Stenerson : Fundamentals Logic Controllers Sensors, & Communications, Englewood Cliffs, NJ, 1993. Prentice Hall.
5. W.Bolton: Programmable Logic Controllers, 5th Edition, Elsevier

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand basics of PLC.
2. Understand basic programming concepts related to PLC.
3. Perform basic PLC functions.
4. Understand SCADA system and its various application areas.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE434C CAD for VLSI
B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (12 Lectures)

Introduction to VLSI Design Methodologies: The Design Domains, Design Methods and Technologies. VLSI Design Automation tools. Algorithmic Graph Theory and Computational Complexity: Data structures, Graph algorithms. Tractable and Intractable problems. General purpose methods for combinatorial optimization.

Unit 2 (11 Lectures)

Design Rules: Layout Compaction: Design rules, Problem formulation, Algorithms for constraint graph Compaction. Placement and partitioning: Circuit representation, Wire-length estimation, Types of placement problems, Placement algorithms, Classification of partitioning algorithm, Performance driven partitioning.

Unit 3 (11 Lectures)

Floor planning and Routing concept: Floor plan representation and optimization problems in floor planning, Shape functions and Floorplan sizing. Classification of Floor planning algorithms, Advancements in Floor planning. Routing: Types of local routing problems, Area routing, Channel routing, Introduction to Global routing, Algorithms for global routing.

Unit 4 (11 Lectures)

Simulation: Type of simulations, Gate-level modeling and simulation, Switch-level modeling and simulation. Logic Synthesis and Verification: Introduction to combinational logic synthesis, Binary Decision Diagrams, Two Level Logic Synthesis. High-level synthesis: Hardware Models

Text/Reference Books:

1. S.H. Gerez, "Algorithms for VLSI Design Automation", John Wiley & Sons.
2. N.A. Sherwani, "Algorithms for VLSI Physical Design Automation", Kluwer Academic Publishers.
3. Sadiq M. Sait, Habib Youssef, "VLSI Physical Design automation: Theory and Practice", World scientific press.
4. Stephen M. Trimberger, "An Introduction to CAD for VLSI" Kluwer Academic Publishers.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand various physical design methods in VLSI and use graph theory approach to VLSI problems.
2. Understand the concepts behind the VLSI design rules for Layout compaction and placement problems.
3. Apply the concepts of various algorithms used for floor planning and routing.
4. Perform the simulation at various levels in VLSI design flow.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines "AICTE Examination Reforms". Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE436C Multimedia Communication

B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (11 Lectures)

Introduction and meaning of multimedia, Medium and its classification, Properties of a Multimedia System, Asynchronous, Synchronous, Isochronous Transmission mode, Data Stream Characteristics for Continuous media.

Basic Sound Concepts: Computer Representation of Sound, Audio Formats, Music: MIDI Basic concepts, devices, messages, MIDI and SMPTE Timing Standards, MIDI Software.

Unit 2 (11 Lectures)

Speech: Generation, Analysis and Transmission.

Images and Graphics: Digital Image Representation, Image Format; Computer Image Processing: Image Synthesis, Image Analysis, and Image Transmission.

Unit 3 (13 Lectures)

Video: Video Signal Representation, vertical Detail and Viewing Distance, Horizontal Detail and Picture Width, Flicker, Temporal Aspect of Video Bandwidth, Bandwidth of NTSC system, Color Encoding Approaches, Digitalization; Computer Video Format, Television-Conventional Systems, Enhanced Definition Systems, High Definition Systems, Composite Coding, Component Coding; Transmission.

Computer Based Animation: Basic Concepts, Animation Languages, Methods of Controlling Animation, Display of Animation, Transmission of Animation.

Unit 4 (13 Lectures)

Data Compression: Storage Space, Coding Requirements; Source, Entropy, and Hybrid Coding; Basic Compression Techniques; JPEG-Image Preparation, Lossy Sequential DCT-based Mode, Expanded Lossy DCT-based Mode, Lossless Mode, Hierarchical Mode.

H.261-Image Preparation, Coding Algorithms, Data Stream; MPEG-Video Encoding, Audio Encoding, Data Stream.

Text Books:

1. Ralf Steinmetz, Klara Nahstedt, "Multimedia: Computing, Communications, & Applications", Pearson.
2. Fred Halsall, "Multimedia Communications: Applications, Networks, Protocols and Standards", Pearson.

Reference Books:

1. Rao, Bojkovic & Milovanovic, "Multimedia Comm. System: Technology, Std. & Network", PHI
2. John F. Koegel Bufod, "Multimedia Systems", Addison Wesley, Edition. 2000

Course Outcomes: After completing this course the students will have the ability to:

1. Understand different types of media and their representation in applications. Analyze and interpret synthetic sound and its use.
2. Generate, represent, analyze and transmit speech and audio.
3. Practically resolve the representation and transmission issues with digital videos for different applications with desired quality.
4. Compress the data of different medias using suitable techniques as per requirement of application.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE484C Project Stage-II
B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits
0	0	16	8

Class Work	: 25
Examination	: 75
Total	: 100
Duration of Exam	: 3 Hours

The project started in VII Semester will be completed in VIII Semester (***applicable for those students only who will not opt for internship in VIII Semester**) and will be evaluated through a panel of examiners consisting of the following:

Head/ Chairperson of Department :	Chairperson
Project coordinator :	Member
External examiner :	To be appointed by the University

The student will be required to submit two copies of his/her project report to the department for record (one copy each for the department and participating teacher).

Project coordinator will be assigned the project load of, maximum of 2 hrs. per week including his own guiding load of one hr. However, the guiding teacher will be assigned maximum of one period of teaching load irrespective of number of students/groups under him/her.

The format of the cover page and the organization of the body of the report for all the B.Tech. courses will be finalized and circulated by the Dean, Faculty of Engineering and Technology.

Course Outcomes: After completing this course, the student will have:

1. Practical exposure of Industrial Projects Skill
2. Learn various aspects of software and hardware handling of industrial work.
3. Attitude more professionally inclined.
4. Better understanding about time management.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

GFECE490C General Fitness for the Profession

B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits	Examination	: 100
0	0	0	-	Total	: 100
				Duration of Exam	: -

The purpose of this course is to inculcate a sense of professionalism in a student along with personality development in terms of quality such as receiving, responding, temperament, attitude and outlook. The student efforts will be evaluated on the basis of his/her performance /achievements in different walks of life.

G.P. (General Fitness For The Profession) is a compulsory & qualifying course (**Audit Pass**) under which student will be evaluated for his performance in all types of activities like Academics, Cultural, Sports, NSS, organisation of camps, social activities etc., during his all 8 semesters, at the end of 8th semester. Regarding this course student will be motivated during the induction programme at the time of admission, so that he/she will be vigilant for motivation towards these activities. The evaluation of the student for his / her General Fitness for the Profession will be carried out by the committee of examiners constituted as under:-

- | | | | |
|----|--|---|-----------------------------|
| 1. | Chairperson of the Department | : | Chairperson |
| 2. | Final Year Coordinator of the Department | : | Member |
| 3. | External Examiner | : | Appointed by the University |

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

CSE340C Artificial Intelligence & Expert Systems

B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1(10 Lectures)

Introduction: The AI problems; what is an AI technique; Characteristics of AI applications Problem Solving, Search and Control Strategies General Problem solving; Production systems; Control strategies: forward and backward chaining Exhaustive searches: Depth first Breadth first search.

Unit 2(10 Lectures)

Heuristic Search Techniques: Hill climbing; Branch and Bound technique; Best first search and A* algorithm; AND/OR Graphs; Problem reduction and AO* algorithm; Constraint Satisfaction problems Game Playing Minmax search procedure; Alpha-Beta cutoffs; Additional Refinements.

Unit 3(12 Lectures)

Knowledge Representation & Reasoning:- Propositional logic, First order predicate logic, Inference in FOPL, Skolemisation; Resolution Principle and Unification; Forward & Backward chaining, Inference Mechanisms Horn's Clauses; Semantic Networks; Frame Systems and Value Inheritance; Conceptual Dependency.

Learning Techniques: - Supervised and unsupervised learning, Decision trees, Statistical learning models, Reinforcement learning.

Expert Systems: Introduction to Expert Systems, Architecture of Expert Systems; Expert System Shells; Knowledge Acquisition; Case Studies: MYCIN, Learning, Rote Learning; Learning by Induction; Explanation based learning.

Text Books/Reference Books:

1. Elaine Rich and Kevin Knight: Artificial Intelligence- Tata McGraw Hill.
2. Dan W.Patterson, Introduction to Artificial Intelligence and Expert Systems- Prentice Hall of India.
3. Nils J.Nilsson: Principles of Artificial Intelligence- Narosa Publishing house.
4. Artificial Intelligence : A Modern Approach, Stuart Rusell, Peter Norvig, Pearson Education.
5. Artificial Intelligence, Winston, Patrick, Henry, Pearson Education.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Analyze and formalize problem and solve them using AI techniques
2. Use Heuristic search techniques for game playing and other problems
3. Represent diverse knowledge using AI and analyze
4. Understand and design an expert system

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines "AICTE Examination Reforms". Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

EE452C Electrical and Hybrid Vehicles

B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits
3	0	0	3

Class Work	: 25
Examination	: 75
Total	: 100
Duration of Exam	: 3 Hours

Unit 1(10 Lectures)

Introduction: History of hybrid and electric vehicles, social and environmental importance of hybrid and electric vehicles, impact of modern electric vehicles on energy supplies.

Electric Vehicle Composition and Configurations, Basic concept of hybrid Electric vehicle, HEV configuration types – series, parallel, series-parallel and complex hybrid, Power flow control.

Unit 2(10 Lectures)

Electric Propulsion: major requirements of EV motor drive, characteristics and control of DC motor, Induction motor, Switched Reluctance motor and Permanent Magnet motor, power converters devices/topology, control hardware, software and strategy vehicle, power source characterization, transmission characteristics.

Unit 3(12 Lectures)

Energy Storage: Introduction to energy storage requirements in Hybrid and Electric Vehicles, Energy sources, Battery based energy storage and its analysis, Fuel cell based energy storage and its analysis, super capacitor based energy storage and its analysis, Flywheel based energy storage and its analysis.

Unit 4(12 Lectures)

Energy Management Strategies: Introduction to energy management strategies used in hybrid and electric vehicles, classification of different energy management strategies, comparison of different energy management strategies, implementation issues of energy management strategies. Plug-in electric vehicles, Vehicle to grid (V2G) and Grid to vehicle (G2V) fundamentals.

Text/ Reference Books:

1. C. Mi, M. A. Masrur and D. W. Gao, “Hybrid Electric Vehicles: Principles and Applications with Practical Perspectives”, John Wiley & Sons, 2011.
2. S. Onori, L. Serrao and G. Rizzoni, “Hybrid Electric Vehicles: Energy Management Strategies”, Springer, 2015.
3. M. Ehsani, Y. Gao, S. E. Gay and A. Emadi, “Modern Electric, Hybrid Electric, and Fuel Cell Vehicles: Fundamentals, Theory, and Design”, CRC Press, 2004.
4. T. Denton, “Electric and Hybrid Vehicles”, Routledge, 2016.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand the basic concept and history of EV and HEV.
2. Understand the models to describe hybrid vehicles and their performance.
3. Understand the different possible ways of energy storage.
4. Understand the different strategies related to energy management systems.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:

Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

MGT401C Entrepreneurship
B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits
3	0	0	3

Class Work	: 25
Examination	: 75
Total	: 100
Duration of Exam	: 3 Hours

Unit 1 (9 Lectures)

Entrepreneurship: Concept and Definitions of Entrepreneur & Entrepreneurship; Classification and Types of Entrepreneurs; Traits/Qualities of an Entrepreneurs; Entrepreneurship's Challenges; Factor affecting Entrepreneurial Growth – Economic & Non-Economic Factors; Entrepreneur Vs. Intrapreneur .EDP Programmes.

Unit 2 (10 Lectures)

Innovation Technology Management: Entrepreneurial Opportunity Search and Identification; recognition of a good business opportunity; Conducting Feasibility Studies. Business Plan: Purpose of Business Plan; Contents of Business Plan; Presenting of Business Plan; Why Business plan Fails.

Unit 3 (10 Lectures)

Indian Models in Entrepreneurship: Social Entrepreneur: Introduction; Characteristics, Need, Types and Motivations of Social Entrepreneur. Women Entrepreneurship: Role & Importance, Profile of Women Entrepreneur, Problems of Women Entrepreneurs, Women Entrepreneurship Development in India.

Unit 4 (9 Lectures)

Developments of Entrepreneur: Micro, Small and Medium Enterprises: Concept & definitions; Role & Importance; MSMED Act 2006, Current Scheme of MSME- Technology Up-gradation Scheme ,Marketing Assistance Scheme ,Certification Scheme, Credit- rating scheme , Problems facing MSME.

Financing the venture: Introduction, features and process of Venture Capital, Funding from Banks.

Text Books:

1. Roy Rajeev, Entrepreneurship 2/e, Oxford University Press.
2. Charantimath, Poornima, "Entrepreneurship Development and Small Business Enterprises", Pearson Education, New Delhi.

Reference Books:

1. Roy Rajeev, Entrepreneurship 2/e, Oxford University Press.
2. Charantimath, Poornima, "Entrepreneurship Development and Small Business Enterprises", Pearson Education, New Delhi.
3. Norman M. Scarborough, "Essentials of Entrepreneurship & Small Business Management", PHI, New Delhi.
4. Vasant Desai, "Entrepreneurial Development and Management", Himalaya Publishing House, New Delhi.
5. Kumar Arya, "Entrepreneurship: creating and leading an entrepreneurial organization", Seventh Impression, Pearson Education.
6. Holt, "Entrepreneurship: New Venture Creation", Prentice Hall, New Delhi.
7. Hisrich, Robert D., Michael Peters and Dean Shepherd, "Entrepreneurship", Tata McGraw Hill, New Delhi.
8. Bridge, S et al., "Understanding Enterprise: Entrepreneurship and Small Business", Palgrave Publication.
9. Donald F. Kuratko, "Entrepreneurship: Theory, Process, and Practice", South Western College Publications.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand the concept of entrepreneurship, traits required to become an entrepreneur.
2. Design and formulate the basic principles of business plans, they can choose and present their business plan.
3. Know about the different types of entrepreneurs.
4. Understand the role of MSME in the development of Small Scale industries.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ME452C Fundamentals Of Sustainable Manufacturing

B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits
3	0	0	3

Class Work	: 25
Examination	: 75
Total	: 100
Duration of Exam	: 3 Hours

Unit 1 (9 Lectures)

Introduction: Introduction to sustainability and drivers for sustainable development and sustainable Manufacturing - Concept of Triple bottom line, Environmental, Economic and Social Dimensions of Sustainability, Sustainable Product Development – Various Phases.

Unit 2 (10 Lectures)

Tools and Techniques: Environmental Conscious Quality Function Deployment, Life cycle assessment, Design for Environment, R3 and R6 cycles, loop production systems, Reverse supply chain, product acquisition management Design for Disassembly.

Unit 3 (10 Lectures)

EIA Standards: CML, EI 95 and 99, ISO 14001 EMS and PAS 2050 standards, Environmental Impact parameters Energy in manufacturing (assessment and minimization)the

Design for recycling: Eco friendly product design methods – Methods to infuse sustainability in early product design phases

Unit 4 (9 Lectures)

Sustainability Assessment: Concept, Models and Various Approaches, Toxic substances in industry, Product Sustainability and Risk/Benefit assessment– Corporate Social Responsibility, Industry cooperation for reducing Carbon footprint

Green Manufacturing: Dry and near-dry machining, edible oil-based cutting fluids, cryogenic machining, improving work environment, of lean manufacturing, Lean techniques for green manufacturing and strategies for waste reduction in green manufacturing.

Text Books:

1. G. Atkinson, S. Dietz, E. Neumayer —Handbook of Sustainable Manufacturingl. Edward Elgar Publishing Limited, 2007.
2. D. Rodick, Industrial Development for the 21st Century: Sustainable Development Perspectives, UN New York, 2007.

Reference Books:

1. P. Lawn, Sustainable Development Indicators in Ecological Economics, Edward Elgar Publishing Limited.
2. S. Asefa, The Economics of Sustainable Development, W.E. Upjohn Institute for Employment Research, 2005.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Summarize sustainability issues and drivers of sustainability.
2. Understand various standards for Environmental Impact Assessment.
3. Apply various tools and technique to access manufacturing sustainability.
4. Comprehend sustainability advantages associated with various manufacturing initiatives.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:
Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

CHE459C Nano-Science And Nano-Technology

B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 25
3	0	0	3	Examination	: 75
				Total	: 100
				Duration of Exam	: 3 Hours

Unit 1 (9 Lectures)

Types of materials; bonding in materials; crystal structures and defects; amorphous materials; origins of properties of materials; Effect of nanostructures on properties of materials.

The science of materials – materials science; Historical use of nanoparticles; discovery of the carbon nanotubes; fullerenes; nanostructured materials.

Unit 2 (10 Lectures)

Particle-wave duality; de-Broglie waves; Schrodinger equation in 1-Dimension; Superposition; Energy eigenstates; Interpretation of wave function; Fermions and Bosons; Electron density of states; Energy bandgaps; Fermi energy; Excitons and Bohr radius.

Unit 3 (10 Lectures)

AFM; STM; Transport in nanostructures; 0,1 and 2 dimensional nanostructures; Bandgap engineering; Molecular motors; MEMS and NEMS devices. Biomaterials and nano-biotechnology.

Unit 4 (9 Lectures)

Synthesis of Nanomaterials – ZnO and Fe₃O₄. Characterization of phases and quantification of phases. Applications of Nanomaterials: In textile industry, in catalytic operations, in energy generation, in energy storage, in environmental remediation and in sensors and devices.

Text Books:

1. NANO:The Essentials Understanding Nanoscience and Nanotechnology, T. Pradeep, Tata McGraw Hill Publishing Company Limited, 2007, 0-07-154830-0.
2. Material Science and Engineering, 7thed. , William D. Callister, Johan Wiley & Sons, Inc.
3. Nanostructured Materials and Nanotechnology, Hari Singh Nalwa, Academic Press, 2002.
4. Nanostructures and Nanomaterials, synthesis, properties and applications., Guozhong Cao, Imperial College Press, 2004.

Reference Books:

1. Introduction to Nanoscience, S.M. Lindsay, Oxford University Press, 2010, ISBN: 978–019–954421–9 (Pbk).
2. Nanoscience, Hans-Eckhardt Schaefer, Springer, 2010, ISBN 978-3-642-10558-6.
3. Chemistry of nanomaterials: Synthesis, Properties and applications. C.N.R. Rao, Achim Muller, A.K. Cheetham, Wiley-VCH, 2004.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Learn about the background on nanoscience and give a general introduction to different classes of nanomaterials.
2. Develop an understanding of the science behind the nanomaterial properties.
3. Apply their learned knowledge to study and characterize nanomaterials.
4. Familiarize themselves with the variety of nanotechnology applications, and know how to approach the synthesis of nanomaterials with a set of desirable properties.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:

Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines “AICTE Examination Reforms”. Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

EE454C Smart Grid
B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits
3	0	0	3

Class Work	: 25
Examination	: 75
Total	: 100
Duration of Exam	: 3 Hours

Unit 1(10 Lectures)

Introduction: Concept of smart grid, smart grid control, Communications and Sensing in a Smart Grid, Hardware Architecture, Software architecture, Protocol detail, application & benefits, PLCs Vs RTUs, IED's, RTU Block diagram, PMU communication interface.

Unit 2(10 Lectures)

Cyber Security of the Smart Grid: Smart Grid Threats, Vulnerabilities and Cyber Security Strategies, Cyber Security Environment, False Data Injection and Attacks in Electric Power Grids Cyber-Physical System Security.

Unit 3(12 Lectures)

Smart Grid Technologies: Energy Management System, Demand side management: peak clipping, valley filling, load shifting etc., state estimation, load forecasting. Time of the day pricing(TOD), Time of use pricing(TOU).

Unit 4(12 Lectures)

Distributed Generation & Control: Concept of distributed generation, Introduction of various distributed generation sources like wind, solar, fuel-cell, micro-hydro, PHEV's etc., Grid integration and control of distributed generation sources.

Text Books:

1. T. Gönen, Electric Power Distribution System Engineering, McGraw-Hill, 1986. ISBN: 0-8493- 5806-X.
2. Distribution System Protection Manual, McGraw-Edison Power Systems, 1990.
3. Westinghouse Electric Utility Ref. Book, Vol.3, Distribution Systems, 1965.
4. R. E. Brown, Electric Power Distribution Reliability, Marcel Dekker Inc., 2002.

Reference Books:

1. IEEE Power and Energy Magazine, July/August 2007 Issue
2. James Burke, Power Distribution Engineering, Mercel Dekker, 1994.
3. A.J. Pansini, Electrical Distribution Engineering McGrawHill, 1983.
4. E. Lakervi, E.J.Holmes, Electricity Distribution Network Design, IEE series, 1989.
5. J. Gers and E. J. Holmes Protection of Electricity Distribution Networks 2nd Edition.

Course Outcomes: At the end of the course, students will demonstrate the ability to:

1. Understand the features of Smart Grid.
2. Understand to make conventional grid more smart, reliable, and efficient.
3. Understand the technical expertise in the emerging area of smart grid.
4. Understand the concepts of distributed generation.

Note:

1. In Semester Examinations, the paper setter will set two questions from each unit (total 8 questions in all), covering the entire syllabus. Students will be required to attempt only five questions, selecting atleast one question from each unit.
2. The use of scientific calculator will be allowed in the examination. However, programmable calculator, mobile phones or other electrical/ electronic items will not be allowed in the examination.
3. For students admitted in B.Tech. 1st Year (C-Scheme) in 2019 & onwards and all trailing students:

Examinations and evaluations of students shall be conducted, covering the entire syllabus, as per guidelines "AICTE Examination Reforms". Students shall be informed about these reforms.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.

ECE488C Internship
B.Tech. 4th YEAR (SEMESTER –VIII)
Electronics & Communication Engineering

L	T	P	Credits	Class Work	: 250
0	0	0	20	Examination	: 250
				Total	: 500
				Duration of Exam	: -

Guidelines for Internship

A student can opt for Internship (minimum 16 weeks) in 8th semester, in lieu of course work of 8th semester, in joint supervision of internal supervisor (allotted by the Department) and the supervisor/official of the organization under whom the candidate is associated for internship. A student can arrange the internship at his/her own and arranging internship for a student by the Department is never his/her right.

Pre-requisite conditions:

- The student has got selected through on-campus/off-campus placement and the same employer is willing to take that student for the Internship.
- The student has got offer of pursuing Internship from Government research organization/govt. sponsored projects IIT'S/IIT'S/IIMs/CDAC.
- The student has got offer of pursuing Internship from reputed private organization.

For pursuing Internship, student will require the prior approval of the Director/Principal of the institute or Chairperson of the University Department. While allowing Internship, the institute/department concerned must insure that the proposed Internship schedule does not disturb the academic calendar in force. The candidate should submit a synopsis of the proposed work to be done during Internship. This synopsis should be submitted to the Department before the start of the internship semester. The synopsis received will be examined/evaluated by the Departmental committee. The student will be allowed for internship only after approval of synopsis by the Departmental committee.

Intimation of commencement of internship shall be submitted to the Chairperson concerned before the commencement of the ongoing semester.

They will have to further deposit the 8th Semester fee. The internship will not be permitted through online mode

If a student feels that the internship work is not of high quality/not-related to their field of interest, then he/ she should submit the application to the Department within two weeks and can re-join the institute to carry out the course work of 8th Semester.

The internal supervisor will monitor the student specific progress of the internship. The overall monitoring of industrial training has to be done by a Departmental Faculty Co-coordinator for Internship.

The Departmental Faculty Co-coordinator will be allotted total weekly teaching load of 2 periods, while each internal supervisor will be allotted total weekly teaching load of 1 period (supervising upto 4 students), and 2 periods , if supervising more than 4 students.

Evaluation Process:

Each student will submit 3 copies of the detailed internship report to the Department in prescribed format at the conclusion of training.

Internal assessment/Sessional of Internship will be made jointly by the Departmental Faculty Co-coordinator for Internship, the concerned organization training supervisor/official and internal supervisor.

Assessment by the External supervisor/Mentor = 40% of Internal Assessment Marks

Assessment by the internal supervisor and Departmental Faculty Co-coordinator for Internship = 60% of Internal Assessment Marks

Practical Examination Assessment of Internship will be made by the committee consisting of the Chairperson of the Department, Departmental Faculty Co-coordinator for Internship and one external examiner appointed by the University.

B. Tech. 4th Year (VII & VIII semester) Electronics & Communication Engg.: Approved in 16th meeting of Academic Council held on 06.10.2021, applicable to all students admitted in 2018-19 & onwards and trailing students.